

WMI — 08:00-12:00 Monday (6/16/08)
High Speed Signal Integrity Workshop with Emphasis on
Jitter

Topics and Speakers:

- 1- Jitter effects, constraints, and mitigation in multi-gigabit links for Terascale computing, Mr. Randy Mooney, Intel Corp.,
- 2- Managing Jitter in High-speed Digital Communications Systems, Mr. Greg LeCheminant, Agilent Technologies
- 3- Time Domain Jitter Measurement Distortion, Mr. Larry Jacobs, LeCroy
- 4- Modeling Jitter and Crosstalk Due To Backplanes Using Modified Rational Functions, Dr. Robert Zeng, The MathWorks, Inc.
- 5- Signal Integrity Panel Session, Dr. Marc Vanden Bossche, NMDG Engineering

Organizers: Mr. Thomas G. Ruttan, Intel Corp, ATTD; Mr. Mike Resso, Agilent Technologies, Component Test Division

Sponsors: MTT-11, MTT-12, ARFTG

Workshop Abstract: Signal integrity is a topic that has generated a significant amount of interest with microwave as well as high speed digital system designers with the emphasis on faster data bus performance that is required for the latest computer and communications systems and products. As the system is analyzed and separated into the key contributors to degradation of the high speed data through the bus structure, jitter stands out as a major factor that drives system performance and design focus. It is important that the designer understand where jitter comes from, how it impacts the system performance, how to model jitter and become familiar with methodologies for measuring jitter. This workshop will focus on these key areas in a full-day workshop that will end with a panel discussion on signal integrity topics (jitter and others) of interest to the workshop attendees.